ELECTRICAL CHARACTERISTICS
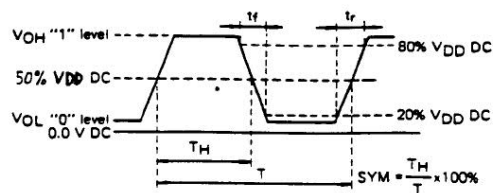
T_A = 25°C, Supply Voltage = 5V ±10%, Output Load = 1 TTL + C_L (15 pF)
C_L = Total fixture and probe capacitance. (Unless otherwise specified)

Frequency	nominal 40 MHz
Frequency stability Including calibration at 23°C, operating temperature range, change of input voltage change of load, chock and vibration and ageing 10 years.	max. ±100 ppm
Calibration tolerance	max. ±20 ppm
Operating temperature range	-25 - +85°C
Storage temperature range	-55 - +125°C
Supply Voltage	5V ±10%
Power consumption	max. 30 mA

Output Voltage
CMOS/TTL compatible
I_L = 1.6 mA V_{OL} = 0.4V max.
I_L = 100µA V_{OH}= 4.0V min

Symmetry   40-60% at 50% V_{DD} (CMOS)
                    40-60% at 1.4 VDC (TTL)

Rise and fall time t_R, t_F
CMOS-levels (20-80% of V_{DD})   max. 20 ns
TTL-levels (0.4V - 2.4V)   max. 10 ns

OUTPUT WAVE FORM

CMOS Output Wave form   **TTL Output Wave form**
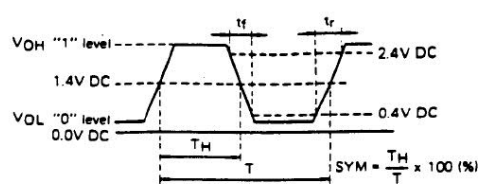
Package   Hermetically sealed metal package

Pin   **Connection**
1   NC or INH*
4   Case, ground   Note: An inhibit function is allowed on pin 1 if it
5   Output   supports the following function:
8        $+V_{DD}$ INH* = OPEN, Output = frequency
                                       INH* = "L", Output = "L"

Outline drawing